



# RE866 Pin Mapping

80555NT11703A Rev. 1 – 2018-07-04

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## APPLICABILITY TABLE

### PRODUCTS

- RE866A1-EU
- RE866A1-NA

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## 1. INTRODUCTION

### 1.1. Scope

This documentation describes the mapping of BlueMod+S42 pins to the nRF52832 pins as required by software developers.

### 1.2. Audience

This document is intended for Telit customers, especially system integrators, about to implement Bluetooth modules in their application.

### 1.3. Contact Information, Support

For general contact, technical support services, technical questions and report documentation errors contact Telit Technical Support at:

- [TS-SRD@telit.com](mailto:TS-SRD@telit.com)

Alternatively, use:

<https://www.telit.com/contact-us>

For detailed information about where you can buy the Telit modules or for recommendations on accessories and components visit:

<https://www.telit.com>

Our aim is to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements.

Telit appreciates feedback from the users of our information.

## 1.4. Text Conventions

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Danger – This information **MUST** be followed or catastrophic equipment failure or bodily injury may occur.

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Caution or Warning – Alerts the user to important points about integrating the module, if these points are not followed, the module and end user equipment may fail or malfunction.

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Tip or Information – Provides advice and suggestions that may be useful when integrating the module.

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All dates are in ISO 8601 format, i.e. YYYY-MM-DD.

## 1.5. Related Documents

- RE866-NA Hardware User Guide, 1VV0301525
- RE866-EU Hardware User Guide, 1VV0301364



## 2. PIN MAPPING

RE866		nRF52832		
MODULE PIN	SIGNAL	QFN48 PIN	NAME	INTERNAL CONNECTED
A1	UART-CTS#	15	P0.12	flex
A2	IUR-IN# Analog In	41	P0.29 AIN5	flex
A3	IUR-OUT#	6	P0.04 AIN2	flex
A4	UART-RXD	16	P0.13	flex
		14	P0.11	flex
A5	UART-TXD	20	P0.17	flex
B1	UART-RTS#	9	P0.07	flex
B2	GPIO[1]	8	P0.06	flex
B4	SWCLK	25	SWCLK	static
B5	NFCANT2	12	P0.10 NFC	static
C1	I2C_SDA Analog In	43	P0.31 AIN7	flex
C2	I2C_SCL	39	P0.27	flex
C4	SWDIO	26	SWDIO	static
C5	SX1272 Reset	18	P0.15 TRCDATA[2]	flex

RE866		nRF52832		
MODULE PIN	SIGNAL	QFN48 PIN	NAME	INTERNAL CONNECTED
C6	BOOT0	17	P0.14 TRCDATA[3]	flex
D5	NFCANT1	11	P0.09 NFC	static
D6	TESTMODE#	10	P0.08	flex
F4	Analog In	5	P0.03 AIN1	flex
G2	LoRa Ant	NA	LORA_ANT	static
G4	EXT-RES#	24	P0.21 RES#	static
		A discrete pull up resistor 470k is used.		
NA Internal	LORA_DIO0	29	P0.24	flex
NA Internal	LORA_DIO1	28	P0.23	flex
NA Internal	LORA_DIO2	27	P0.22	flex
NA Internal	LORA_CS	4	P0.02 / AIN0	flex
NA Internal	RFSW_CTRL	7	P0.05 / AIN3	flex
NA Internal	nRF_SW_VCC	19	P0.16	flex
NA Internal	LORA_MOSI	42	P0.30 / AIN6	flex
NA Internal	LORA_MISO	40	P0.28 / AIN4	flex
NA Internal	LORA_SCK	38	P0.26	flex

RE866		nRF52832		
MODULE PIN	SIGNAL	QFN48 PIN	NAME	INTERNAL CONNECTED
NA Internal	SAFE_SCL	23	P0.20	flex
NA Internal	SAFE_SDA	22	P0.19	flex
NA Internal	SAFE_RSTn	21	P0.18	flex
NA Internal	XL-IN Slow CLK 32. 768 kHz	2	P0.00 / XL1	flex
NA Internal	XL-OUT Slow CLK 32. 768 kHz	3	P0.00 / XL2	flex
NA Internal	XIN 32 MHz	34	XC1	static
NA Internal	XOUT 32 MHz	35	XC2	static

### 3. GLOSSARY AND ACRONYMS

TTSC	Telit Technical Support Centre
USB	Universal Serial Bus
NFC	Near Field Communication
DTE	Data Terminal Equipment
UART	Universal Asynchronous Receiver Transmitter
SPI	Serial Peripheral Interface
ADC	Analog – Digital Converter
DAC	Digital – Analog Converter
I/O	Input Output
GPIO	General Purpose Input Output
CLK	Clock
MOSI	Master Output – Slave Input
MISO	Master Input – Slave Output
CS	Chip Select
RTC	Real Time Clock

#### 4. DOCUMENT HISTORY

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Revision	Date	Changes
0	2018-07-03	First issue
1	2018-07-04	Add crystal connections, correct typos



# SUPPORT INQUIRIES

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