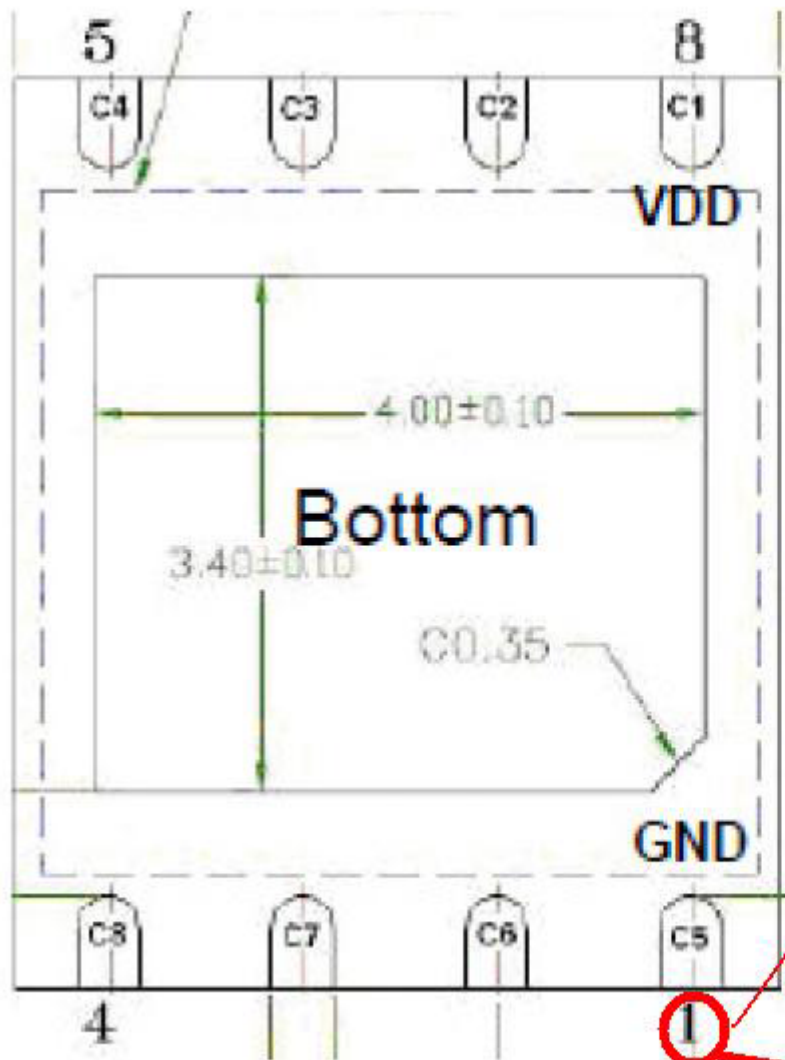


# ВИД НА МИКРОСХЕМУ СНИЗУ



### PINOUPS

- Pin #01 = Vss (GND)
- Pin #02 = NA
- Pin #03 = I/O 0
- Pin #06 = CLK
- Pin #07 = RST
- Pin #08 = VDD

The default pinout configuration proposed is compliant with MFF2 pinout as per ETSI TS 102 671



Первая ножка